

Amendment to the Abstract

Please replace the Abstract on page 42 of the application with the following Abstract:

An architecture, circuits, systems and a method for amplifying an analog signal. The architecture and/or circuit generally includes ~~(a) a predriver~~ first fixed stage (e.g., a predriver) and an adjustable stage. The first fixed stage may be configured to amplify an analog signal and provide [[an]] a first amplified analog output at a first common node, ~~and (b) an~~ The adjustable stage comprising may comprise a plurality of independently selectable parallel amplifier segments. Each of the parallel segments may have an input at the first common node and an output at a second common node, a transistor having a control terminal, and a first inductor in electrical communication with the control terminal of the transistor. The adjustable stage may be configured to apply a bias to the control terminal of the transistor in a selected segment and to provide[[s]] an output signal in one of a plurality of a power ranges corresponding to the a number of selected parallel amplifier segments. The output signal generally has a minimum power efficiency when two or more of the parallel segments are selected. The systems generally include the architecture, circuit or an integrated circuit that embodies one or more of the inventive concepts disclosed herein. The method generally includes the steps of amplifying the analog signal in a fixed amplifier stage, selecting a number of parallel amplifier segments for subsequent signal amplification, and amplifying the amplified analog signal with the activated parallel, selectable amplifier segments to generate an output signal in a unique output power range corresponding to the number of selected parallel amplifier segments. The present invention advantageously provides a relatively compact power amplifier with an extended output power range at which the amplifier is highly efficient. In preferred embodiments, the input and output matching characteristics are generally independent of the number of selected output amplifier segments.